

PATENT  
PRELIMINARY AMENDMENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Thomas M. Gooding et al. : Date: August 31, 2007  
Group Art Unit: 2128 : IBM Corporation  
Examiner: Kimberly A. Thornewell : Intellectual Property Law  
Serial No.: 10/757,788 : Dept. 917, Bldg. 006-1  
Filed: January 15, 2004 : 3605 Highway 52 North  
Title: METHOD AND APPARATUS FOR : Rochester, MN 55901  
THE AUTOMATIC CORRECTION  
OF FAULTY WIRES IN A LOGIC  
SIMULATION HARDWARE  
EMULATOR / ACCELERATOR

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

PRELIMINARY AMENDMENT

Please amend the above-identified application as follows: